





connector 2

PE7	1	PE6	2
PE5	3	PE4	4
PE3	5	PE2	6
PE1	7	PE0	8
VCC 33V	9	GND	10
PC4 MISO	11	PC5 MISO	12
PC7 T2OUT	13	PC8 T2IN	14
PC3 SCK	15	PC2 S2	16
PC0 T1IN	17	PC0 T0IR	18
GND	19	PA1 T0OUT	20
PC1 T1OUT	21	PA2	22
PD1 T3OUT	23	PD6 CTST	24
PD3	25	PD4 RXD1	26
PD5 TXD1	28	PD0 T3IN	29
PD7 RCOUT	29	GND	30
PF0	31	PD2	32
PA3 CT50	33	PC0	34
PA4 RXD0	36	PA5 TXD0	37
GND	38	NC	39
NC	40	NC	41
NC	42	NC	43
PA6 SCL	44	NC	45
PA7 SDA	46	GND	47
NC	48	NC	49
NC	50	NC	51
NC	52	DIS_IRDA	53
NC	54	NC	55
-RESET	57	NC	58
VCC 33V	58	GND	59
VCC 33V	59	NC	60

HEADER 30x2/SM

If Module is plugged onto the Dev Platform the local RS232 interface is disabled by pin 50 of JP2

- PG1 Header 1 JP15
- PG2 Header 1 JP16
- PG3 Header 1 JP17
- PG4 Header 1 JP18
- PG5 Header 1 JP19
- PG6 Header 1 JP20
- PG7 Header 1 JP21

connector 1 for reference only

1	2
3	4
5	6
7	8
9	10
VCC 33V	VCC 33V
12	A0
13	A3
14	A9
15	VCC 33V
16	A7
17	A8
18	A9
19	A4
20	A14
21	A16
22	A18
23	A19
24	A27
25	A1
26	A12
27	A20
28	A17
29	A33
30	A33
31	A33
32	A33
33	A33
34	A33
35	A33
36	DIS_FLASH
37	VCC 33V
38	A23
39	A23
40	A23
41	A23
42	A23
43	A23
44	A23
45	A23
46	A23
47	A23
48	A23
49	A23
50	A23
51	A23
52	A23
53	A23
54	A23
55	A23
56	A23
57	A23
58	A23
59	A23
60	A23