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Product Update

Errata for Z16C32 IUSC™ Integrated Universal Serial Controller User Manual

Introduction

This product update corrects a typographical error found on page 8-16, Register Reference section, Z16C32 IUSC™ Software Summary for *Z16C32 IUSC Integrated Universal Serial Controller User Manual (UM0140)* available for download at www.zilog.com.

Discussion

In *UM0140* on page 8-16, under Register Reference section, there is an error in the Channel Mode Register (CMR) table. The description for Bit CMR5, RxSubmode, states:

0=strip received Syncs; **0**=include them in RxFIFO and CRC calculation

The statement should read as follows:

1=strip received Syncs; **0**=include them in RxFIFO and CRC calculation



Warning: DO NOT USE IN LIFE SUPPORT

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