



An IXYS Company

UP013601-0810

## Product Update

**Errata for eZ801900200ZCOG/  
eZ80L920210ZCO/eZ80F920200ZCOG/  
eZ80F910300ZCOG**

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The errata listed in [Table 1](#) is found in the eZ801900200ZCOG/eZ80L920210ZCO/eZ80F920200ZCOG/eZ80F910300ZCOG development kits. When reviewing the following errata, it is recommended that you download the most recent version of the user manuals from [www.zilog.com](http://www.zilog.com):

- *eZ80190 Development Kit User Manual (UM0141)*
- *eZ80L92 Development Kit User Manual (UM0129)*
- *eZ80F91 Development Kit User Manual (UM0142)*
- *eZ80F92 Development Kit User Manual (UM0139)*

**Table 1. eZ801900200ZCOG/eZ80L920210ZCO/eZ80F920200ZCOG/eZ80F910300ZCOG  
Errata**

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No.	Summary	Detailed Description
1	External SRAM data corruption	<p>The eZ80<sup>®</sup> has a very tight timing for read and write operations. Because of the differences of the speed of the address and read/write drivers on the main board, the data in the external SRAM on the main board can be corrupted.</p> <p><b>Workaround</b></p> <p>1. To fix this issue, slower drivers (74LV244A) are recommended on the address bus to increase the address hold time, which fixes the SRAM corruption. In the current user manual schematic, U1, U3, and U5 use the 74LVC244A device; 74LVC244A must be replaced with 74LV244A.</p> <p>2. Use Intel Bus Mode instead of the eZ80 Bus Mode to increase the address hold time. This is done by changing bit 7 and bit 6 in the Chip Select Bus Mode Control Register from 00b to 10b. There is a description of Intel Bus Mode in the “Chip Selects and Wait States” chapter in the product specification in the “Intel Bus Mode” section.</p>

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**Warning:** DO NOT USE IN LIFE SUPPORT

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