

### Errata for Z8400/Z84C00 NMOS/CMOS Z80 CPU Devices

This Errata provides information on the 44-lead QFP which has been replaced by the 44-lead LQFP.

#### Affected Documents

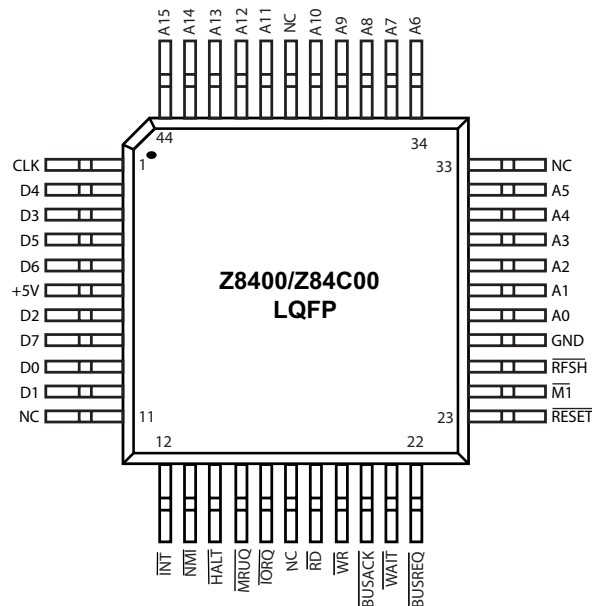
Table 1 provides the list of documents affected by the replacement of 44-lead QFP with 44-lead LQFP.

**Table 1. Documents Affected by the Replacement of 44-lead QFP with 44-lead LQFP**

Devices	Title	Document Number
Z8400/Z84C00	Z8400/Z84C00 NMOS/CMOS Z80 CPU Central Processing Unit Product Specification	PS0178

#### 44-Lead LQFP

The Z8400/Z84C00 NMOS/CMOS Z80 CPU devices are now available in 44-lead LQFP. Figure 1 displays the pin diagram and Table 2 provides the pin description of the 44-lead LQFP.



**Figure 1. 44-Lead Z8400/Z84C00 LQFP**

**Table 2. 44-Lead Z8400/Z84C00 LQFP Description**

Pin No	Symbol	Function	Direction
1	CLK	Clock	Input
2–5	D4–D6	Data Bus 4, 5, 6	Input/Output
6	+5V	Power Supply	Input
7	D2	Data Bus 2	Input/Output
8	D7	Data Bus 7	Input/Output
9–10	D0–D1	Data Bus 0, 1	Input/Output
11	NC	No Connection	N/A
12	$\overline{\text{INT}}$	Interrupt Request	Input
13	$\overline{\text{NMI}}$	Non Maskable Interrupt	Input
14	$\overline{\text{HALT}}$	Halt State	Output
15	$\overline{\text{MREQ}}$	Memory Request	Output
16	$\overline{\text{IORQ}}$	Input/Output Request	Output
17	NC	No Connection	N/A
18	$\overline{\text{RD}}$	Read	Output
19	$\overline{\text{WR}}$	Write	Output
20	$\overline{\text{BUSACK}}$	Bus Acknowledge	Output
21	$\overline{\text{WAIT}}$	Wait	Input
22	$\overline{\text{BUSREQ}}$	Bus Request	Input
23	$\overline{\text{RESET}}$	Reset	Input
24	$\overline{\text{M1}}$	Machine Cycle One	Output
25	$\overline{\text{RFSH}}$	Refresh	Output
26	GND	Ground	Input
27–32	A0–A5	Address Bus 0, 1, 2, 3, 4, 5	Output
33	NC	No Connection	N/A
34–38	A6–A10	Address Bus 6, 7, 8, 9, 10	Output
39	NC	No Connection	N/A
40–44	A11–A15	Address Bus 11, 12, 13, 14, 15	Output



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