



ZDI23200ZPK

ZPAK DATASHEET

OT003200-SEC0100

OVERVIEW

Congratulation for selecting a fine development tool! ZPAK provides full emulation for ZiLOG processors equipped with on-chip emulation support hardware. ZPAK uses the ZDI23200ZPK board and provides the physical link between the ZiLOG Developer Studio (ZDS) and the user's target system. Using the ZiLOG debugger interface protocol (ZDI), the user can download code directly to the target system to edit and debug code.

ZPAK comes equipped with LED indicators to communicate ZPAK's status to the user. ZPAK is designed to be used with the ZiLOG Developer Studio, giving the user a total package to write, edit and debug applications.

EMULATOR FEATURES

Key features of ZPAK include:

- 40-MHz Microprocessor providing:
 - Serial communication to host PC
 - Debug monitor output used to assist in firmware development
 - I/O mapped ZDI control register interface
- 128-KB FLASH memory
- 512-KB SRAM
- Hardware shift register implementing ZDI protocol
- 6-Pin header for ZPAK target connection
- DB9 female RS-232 connector for host interface communication
- 10-Pin header providing RS-232 debug monitor output

SUPPORTED ZILOG DEVICES

ZPAK supports any ZiLOG microprocessor that features on-chip ZDI emulation support hardware.

HARDWARE SPECIFICATIONS

Operating Conditions

Operating Humidity	10%–90% RH (non-condensing)
Operating Temperature	20°C ± 10°C
Clocks	Control processor: 7.3728 MHz Emulation Processor: 8 MHz
Serial Baud Rate	57,6000 bps

Power Requirements

This emulator requires an external 9 VDC power supply.

Interface

ZiLOG Developer Studio communicates with ZPAK using a DB25, RS-232 and DCE cable (TxD, RxD only).

Connection to the target board is achieved using 6-pin female-to-female ribbon cable assembly.

GUI-Supported Compiler, Assembler Formats

The emulator supports object (binary or Intel hex) code files produced by the ZiLOG Developer Studio and ZiLOG Macro Cross Assemble (ZMASM).



Kit Contents

The ZPAK kit contains one of each of the following items:

- ZPAK and enclosure
- External 9 Volt DC power supply
- 12-inch, 6-pin female-to-female ribbon cable assembly
- DB9 male to DB9 female RS-232 cable
- ZiLOG Developer Studio installation CD
- ZiLOG Debug Interface User's Manual
- ZiLOG Developer Studio manual contained on the ZDS installation CD-ROM

The ZDS CD-ROM can also be downloaded from the ZiLOG web site at:

www.zilog.com

Additional Required Items Not Supplied With This Support Package

The ZPAK is designed to interface with all ZiLOG products that contain a ZDI interface. The evaluation board to each individual device is sold separately.

The user's target design that ZPAK plugs into is not supplied by ZiLOG.

Optional Recommended Items

The following items are recommended:

- Oscilloscope
- Logic analyzer