



UP005001-0103

Product Update

Errata to Z86E06 and Z86C06 Product Specifications

INTRODUCTION

This Errata provides additional clarifying information for the Serial Peripheral Interface (SPI) feature of the Z86C06 and Z86E06 subfamily of Microcontroller devices.

AFFECTED DOCUMENTATION

Devices	Title	Document Number
Z86C03/C06	8-Bit CMOS Z8 [®] MCU	DS96Z8X0902
Z86E03/E06	CMOS Z8 OTP [®] Microcontrollers	PB007301-0701

SERIAL PERIPHERAL INTERFACE

The Z86C06 and Z86E06 subfamily of Microcontrollers does not support the Serial Peripheral Interface (SPI) feature in slave mode. The Z86C06 and Z86E06 subfamily of Microcontrollers does support the Serial Peripheral Interface (SPI) feature in master mode but does not support SPI interrupts.

Software Workaround

Software workaround for the master mode SPI interrupt:

After the load instruction to the SPI shift register, the transfer is completed after four NOP's with the SPI set to the highest clock speed of TCLK/2. Seven NOP's are required if the clock speed is set to TCLK/4.



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